IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

REVOCATION OF POWER OF ATTORNEY, NEW POWER OF ATTORNEY AND NOTICE OF CHANGE OF CORRESPONDENCE ADDRESS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

For: Marvell International Ltd.

Dear Sir:

Marvell International Ltd., the owner of the entire right, title and interest in the attached listing of U.S. patents and pending applications hereby revokes all Powers of Attorney previously given in such patents and patent applications. Marvell International Ltd. is the Assignee of the entire right and interest in each of these U.S. patents and pending applications, as evidenced by the documentary evidence of the chain of title from the original owner to the Assignee, recorded in the Assignment records of the office at the reel/frame numbers indicated on the attached sheet.

Marvell International Ltd. hereby appoints the attorneys and/or agents associated with customer number 64768 to prosecute these patents and patent applications and transact all business in the U.S. Patent and Trademark Office.

Please send all correspondence and direct telephone calls to the following:

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(312) 474-6300

The undersigned is authorized to act on behalf of Marvell International Lt.

lame: CAROI FEATHERS

THE CAROI MANAGER

Marvell International Ltd. U.S. Patents

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Assignment	Keel/Frame No.	000844/0863	7000/110000	018515/0817		009672/0397			018515/0817		010419/0894			018515/0817					010603/0899			018515/0817	
	To	INTEL CORPORATION	MARVETT	INTERNATIONAL	LTD.	INTEL	CORPORATION	MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION		MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION			MARVELL	INTERNATIONAL	LTD.
Assignment	From	CLARK, LAWRENCE	NTFI	CORPORATION		O'CONNOR,	DENNIS M.	INTEL	CORPORATION		CLARK,	LAWRENCE T.;	TSANGARIS, E.J.M.	INTEL	CORPORATION		CLARK	LAWRENCE T.;	CLARK,	MATTHEW M.	INTEL	CORPORATION	
Title	5 Volt Tel 42 17-14 FT FT FT FT FT FT FT FT	3-Voit 10lerant 3-Voit Drive Push-Pull Buffer/Driver				Method and Apparatus for Speeding	Sequential Access of a Set-Associative				Register File with Improved Noise	Immunity and Aspect Ratio					Method and Apparatus to Perform a	Round Robin and Locking Cache	Replacement Scheme				
Attorney/ Docket No.	MP1520	67C1 JIM			100100	MF1526					MP1530						MP1532						
Patent Date	3/13/2001	10041010			1000/00/3					100000	1007/87/8					20000	2/4/2003						
Patent No.	6.201.428	6			K 227 007					001.000	0,282,139					100 713 7	495,010,0						

	Assignment	Recl/Frame No.	010629/0127			018515/0817		012609/0901				018515/0817		014246/0808		018515/0817		
	ment	To	INTEL	CORPORATION	MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION		MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION	MARVELL	INTERNATIONAL	LTD.
	Assignment	From	O'CONNOR,	DENNIS M.	INTEL	CORPORATION		FRANCO, AMIR;	YELLIN, DANIEL;	GURESHNIK, ERAN	INTEL	CORPORATION		TANG,	PING T.	INTEL	CORPORATION	
Tide		Mathod and A	Michigal and Apparatus to Encode	Digital Signais			1	Time Delay Estimation in a Transmitter					Mostord and A	Medical and Apparatus for Measuring	Accuracies of Fixed-Point	Computations		
Attorney/	Docket No.	MP1531	ICCI TAT				MD1520						MP1486	2001				
Patent	Date	4/1/2003					2/21/2006	2001					4/18/2006					
Patent No.		6,543,022					7.003.051						7.031.885					

Assignment	
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Patent	
Patent No.	

Reel/Frame No	014533/0696	018515/0817	014224/0304	018515/0817	012793/0866	018515/0817
To	CORPORATION	MARVELL INTERNATIONAL	INTEL	MARVELL INTERNATIONAL	INTEL	MARVELL INTERNATIONAL LTD.
From	RAYKAR, VIKAS C.; KOZINTSEV, IGOR; LIENHART, RAINER	INTEL	KHAN, MOINUL H. PANG, SWEE- CHIN	INTEL CORPORATION	ERELL, ADORAM	INTEL CORPORATION
	Three-Dimensional Position Calibration of Audio Sensors and Actuations on a Distributed Computing Platform		Dynamic TLB Locking Based on Page Usage Metric		MP1540.11 Enhancing the Intelligibility of Received Speech in a Noisy	Environment
Docket No.	MP1488		MP1487		MP1540.11	
	4/25/2006	1	7/25/2006		8/8/2006	
	7,035,757		7,082,508		7,089,181	

	Assignment	Reel/Frame No.	016024/0112					018515/0817		
	ment	To	INTEL	CORPORATION				MARVELL	INTERNATIONAL	LTD
	Assignment	From	STEVENSON, PAUL	т,	TOURVILLE, JON E.;	PIHLSTON, NATHAN	L.	INTEL	CORPORATION	
· Filt	THE		Crystal Oscillator with Variable Bias	Generator and Variable Loop Filter						
Attomes/	Dooket No	DOCKEL INO.								
Patent	Date	Cate	',123,109 10/17/2006 MP1518							
Patent No.		20,000	7,123,109							

Marvell International Ltd. U.S. Patent Applications

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Assignment	Reel/Frame No.	013468/0665	014047/0317	018515/0817	013627/0281	014047/0317	018515/0817	014002/0599	018515/0817
ment	To	D.S.P.C. TECHNOLOGIES LTD.	INTEL	MARVELL INTERNATIONAL LTD	D.S.P.C. TECHNOLOGIES LTD.	INTEL CORPORATION	MARVELL INTERNATIONAL LTD.	INTEL	MARVEL INTERNATIONAL LTD
Assignment	From	MAZUZ, GADI; GREISAS, YUVAL;	D.S.P.C. TECHNOLOGIES LTD.	INTEL CORPORATION	ARONOWITZ, HAGAI	D.S.P.C. TECHNOLOGIES LTD.	INTEL	DARROUDI, KHOSRO; MEARS, BRIAN R.	INTEL
Title		Cipher Implementation			Method and Apparatus for Adapting Reference Templates			Speech Compression Method and Apparatus	
	۱ ۵	MP1546			MP1473			MP1475	
Application	rumg Date	11/4/2002			12/24/2002			1/3/2003	
Application		10/286,800			10/327,058		000	10/336,668	

Assignment	Reel/Frame No.	013878/0351	018515/0817	014664/0419	018515/0817	014875/0896	018515/0817	015114/0345	018515/0817
ment	To	INTEL	MARVEL INTERNATIONAL I.TD	INTEL	MARVELL INTERNATIONAL	INTEL	MARVELL INTERNATIONAL LTD.	CORPORATION	MARVELL INTERNATIONAL LTD.
Assignment	From	O'CONNOR, DENNIS	INTEL CORPORATION	YELLN, DANIEL	INTEL CORPORATION	SUTSKOVER, ILAN; BEN-ELI, DAVID	INTEL	MUNTER, JOEL D.; NANDA, MUTHI; GUO, ZHIYAO; XU, JIN J.	INTEL
Title		Register File with Integrated Routing to Execution Units for Multi-Threaded Processors		Unified MMSE Equalization and Multi- User Detection Approach for Use in a	CDMA System	Transmitter Operations for Interference Mitigation		Power and/or Energy Optimized Compile/Execution	
Attorney	Docket No.	MP1481		MP1483		MP1484		MP1502	
Application Application		3/11/2003		10/697,853 10/30/2003		12/29/2003		3/17/2004	
Application	No.	10/388,384		10/697,853		10/749,693		10/802,586	

Assignment	Reel/Frame No.	014869/0564	014869/0875	014869/0561	018515/0817	015168/0523			018515/0817
nment	To	INTEL	INTEL CORPORATION	INTEL CORPORATION	MARVELL INTERNATIONAL	LTD. INTEL	CORPORATION		MARVELL INTERNATIONAL
1	From	HSIEH, CHENG- HSUEH	BEYLIN, BORIS	DAI, JASON	INTEL	BIBIKAR,	VASUDEV J.; FULLERTON,	MARK N.; FEDDELER, JAMES R.	INTEL
2011	Debugging Tool fe-D-t	Coorgans 1001 to Debugging Mutt- Threaded Programs				Power Supply detection Method,	Apparatus, and System		
						MP1506			
Filing Date						6/30/2004			
Ņ.	10/835,083					10/880,762			
	Filing Date Docker No	Docket.N; Assignment	Docket No.	MP1499 Debugging Tool for Debugging Multi- From From From To	Decket No. MP1499 Debugging Tool for Debugging Multi- From From To	Drocket No. Assignment From From To	MP1499 Debugging Tool for Debugging Multi- From From Tool	NET Assignment From Assignment	NP1499 Debugging Tool for Debugging Multi- From Assignment To Assignme

	Assignment	Pool/Erama Mo	015582/0651						018515/0817		018197/0599		018515/0817		
	nent	То	INTEL	CORPORATION				MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION	MARVELL	INTERNATIONAL	Ē
	Assignment	From	ANDERS, MARK A.	MATTHEW, SANU	.;	KRISHNAMURTHY,	RAM K.	INTEL	CORPORATION		ERELL, ADORAM		INTEL	CORPORATION	
1.18	liffe		Distributed Ring Control Circuits for	Viterbi Traceback						17.1	ivir 12401101 Ennancing the Intelligibility of Received	Speech in a Noisy Environment			
Attomost	Destreta	Docket No.	MP1519							MEDICADIO	MI 134011C1				
Application Application	Filing Date		9/30/2004		-					8/0/0/6/8	2007				
Application	Z		10/954,648							11/498 458	20.				